

REPLACEMENT SHEET

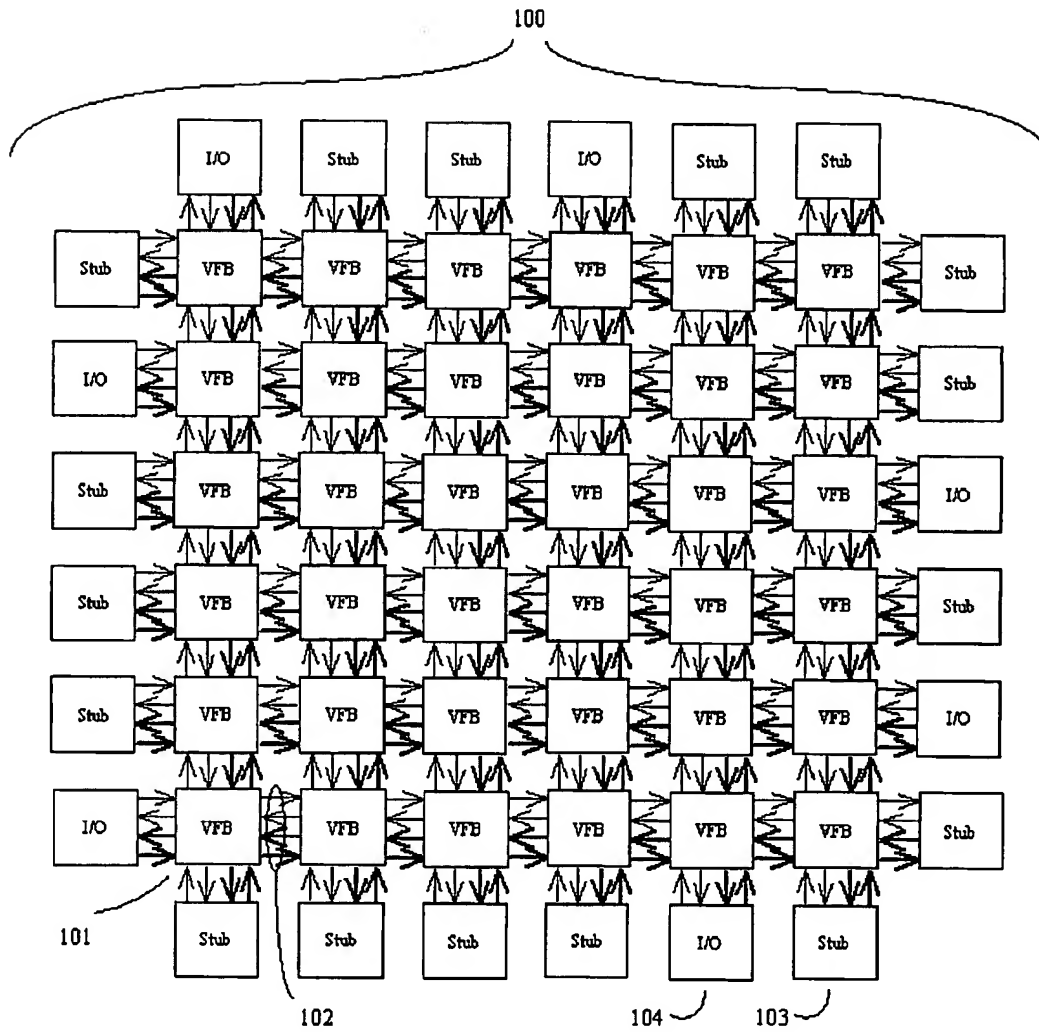


Figure 1, Messenger Interconnected Processor Array

Figure 2, Individual VFB

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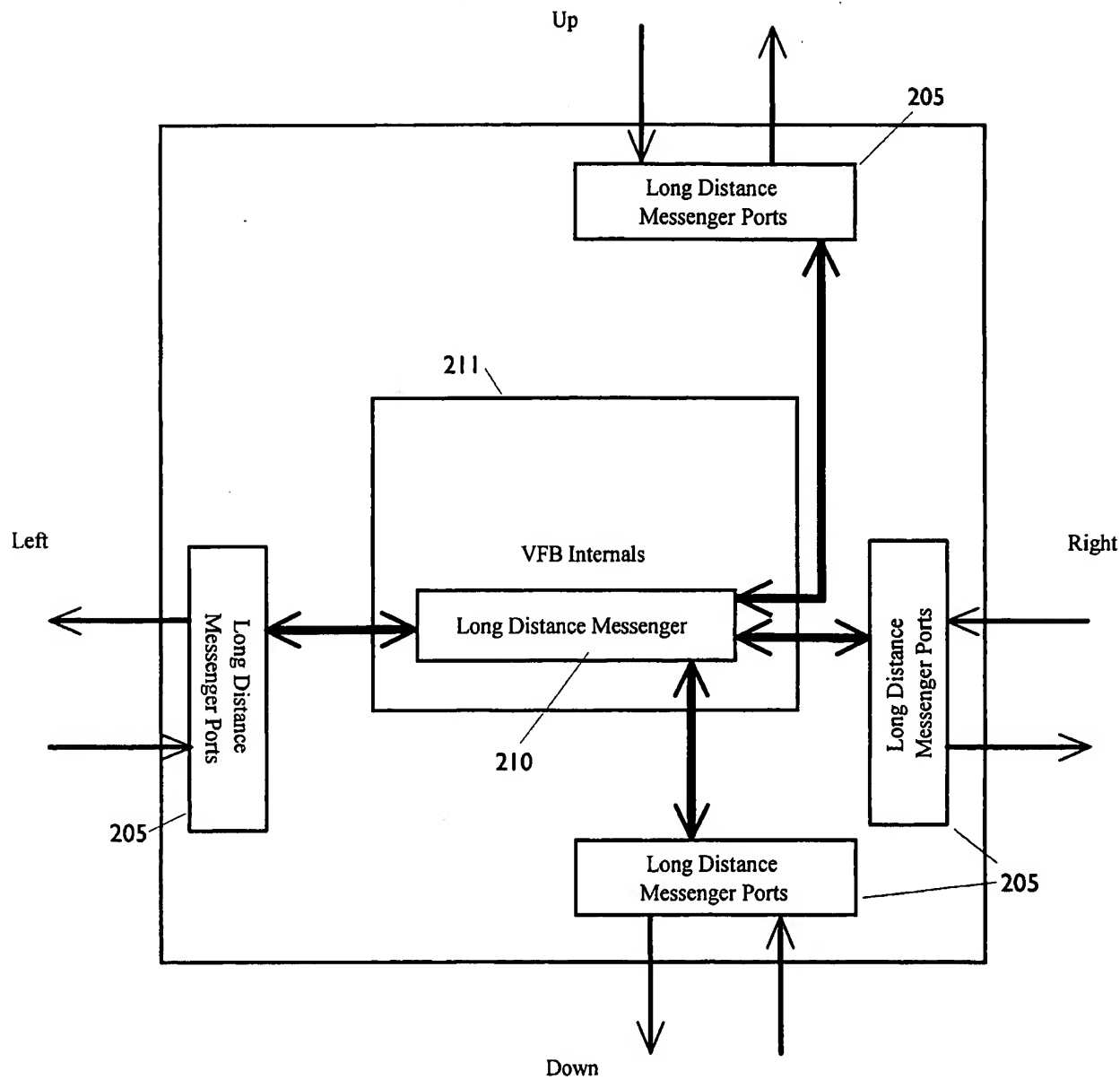


Figure 2A, Individual VFB

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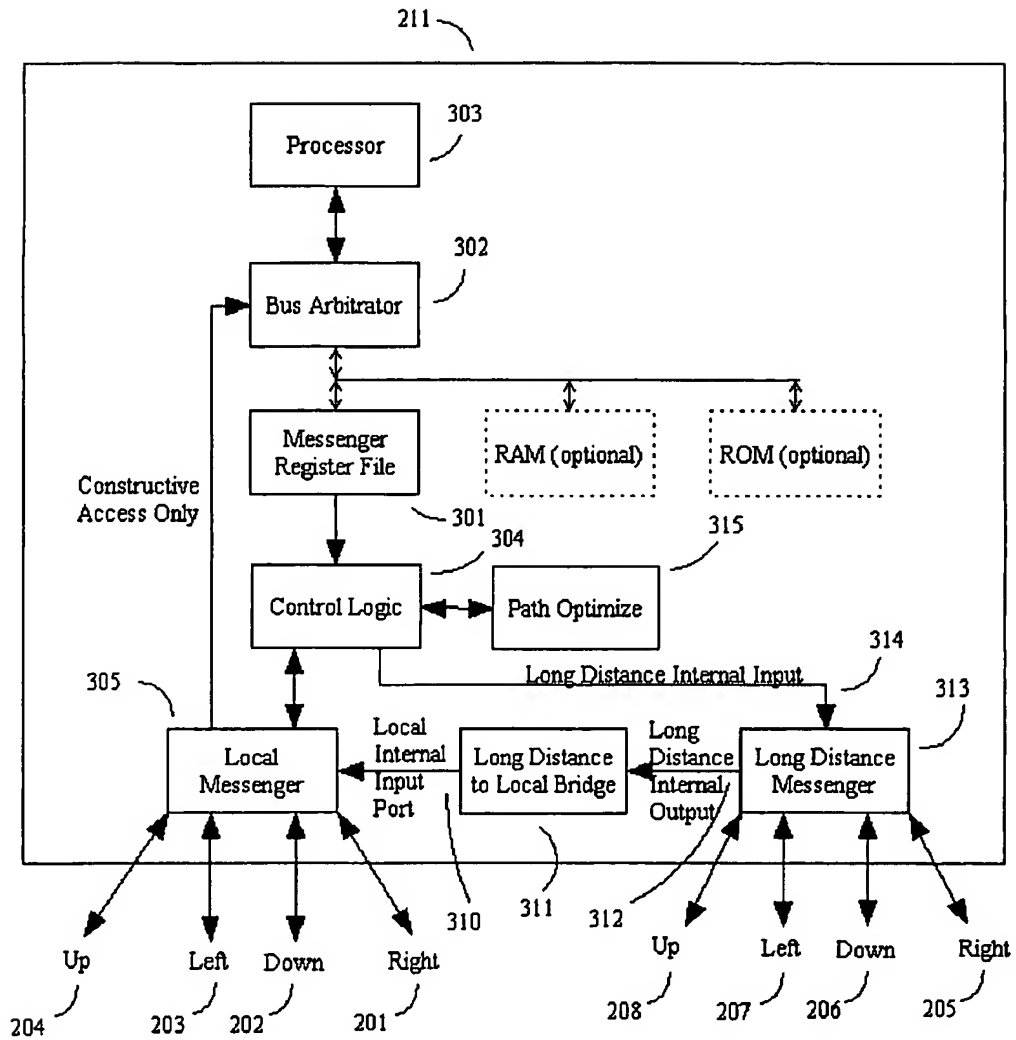


Figure 3, VFB Internals

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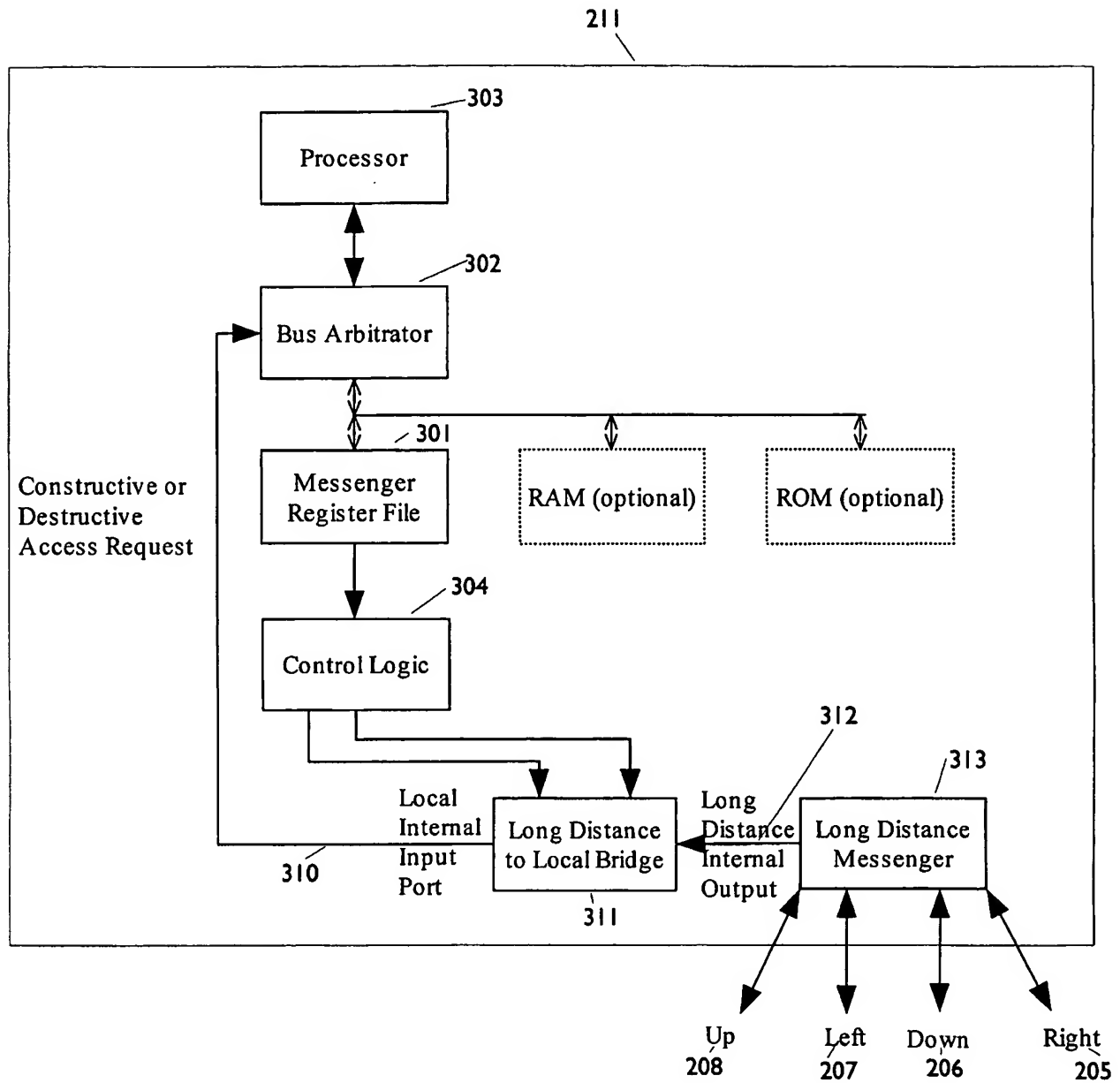


Figure 3A, VFB Internals

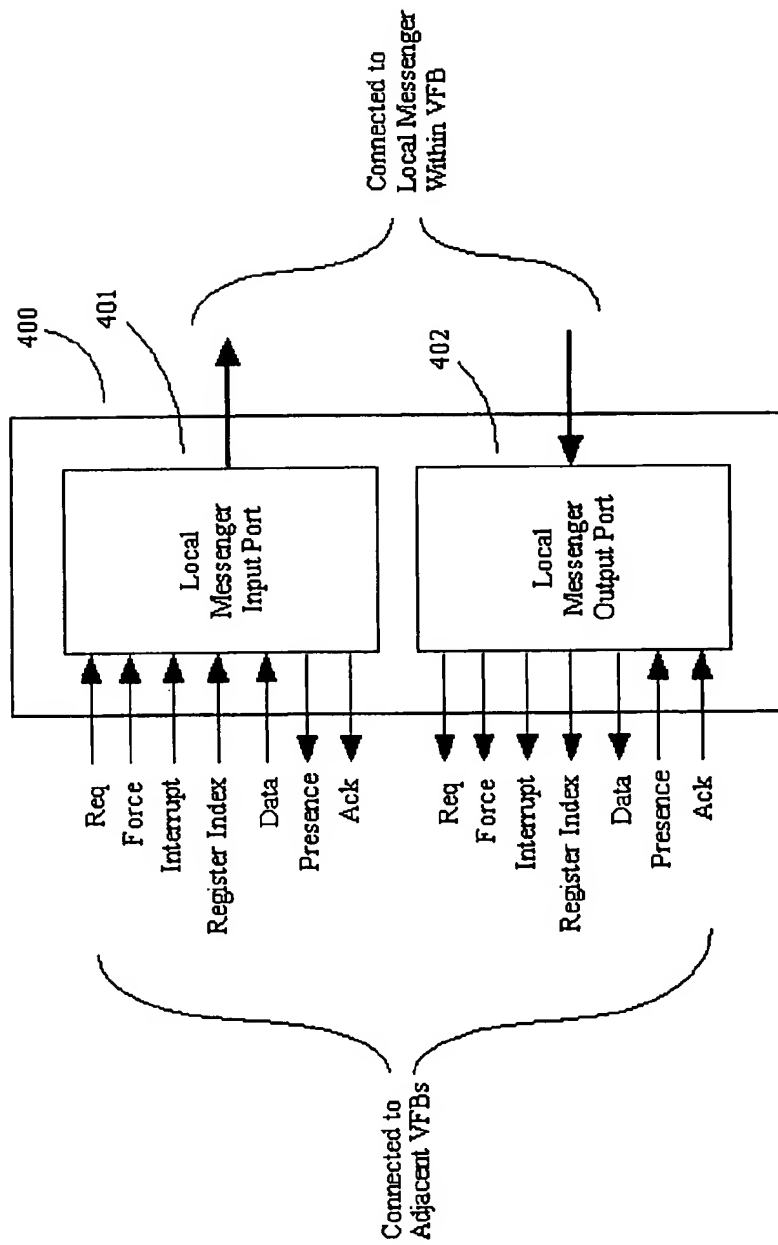


Figure 4, Local Messenger Port

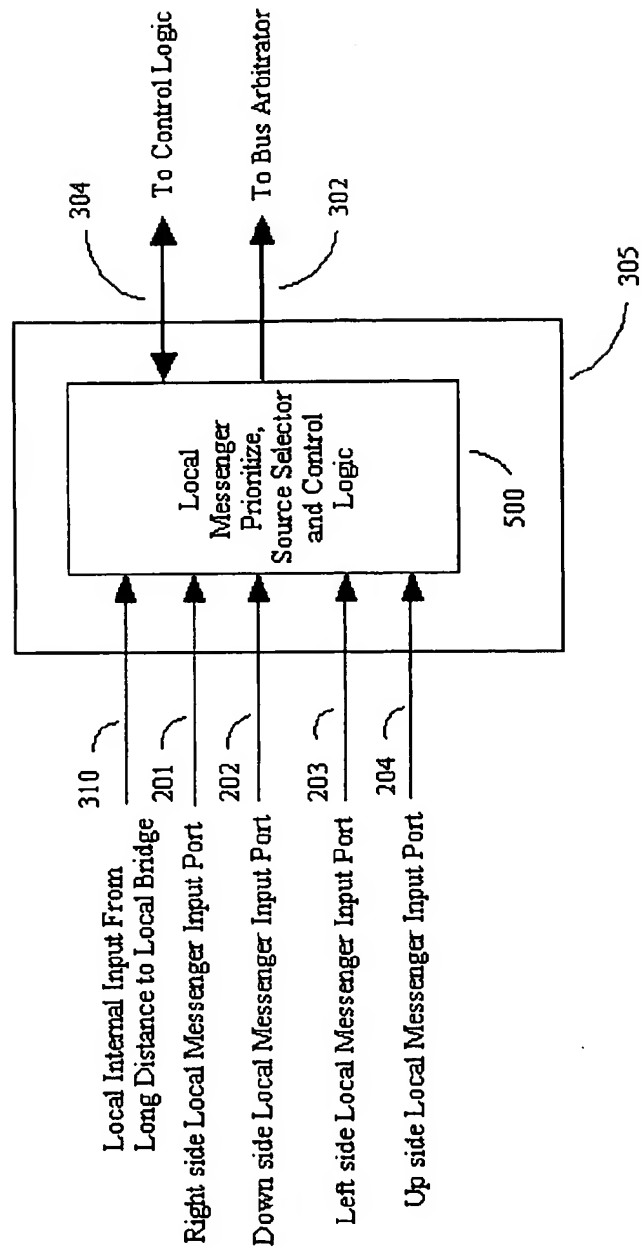


Figure 5, Local Messenger

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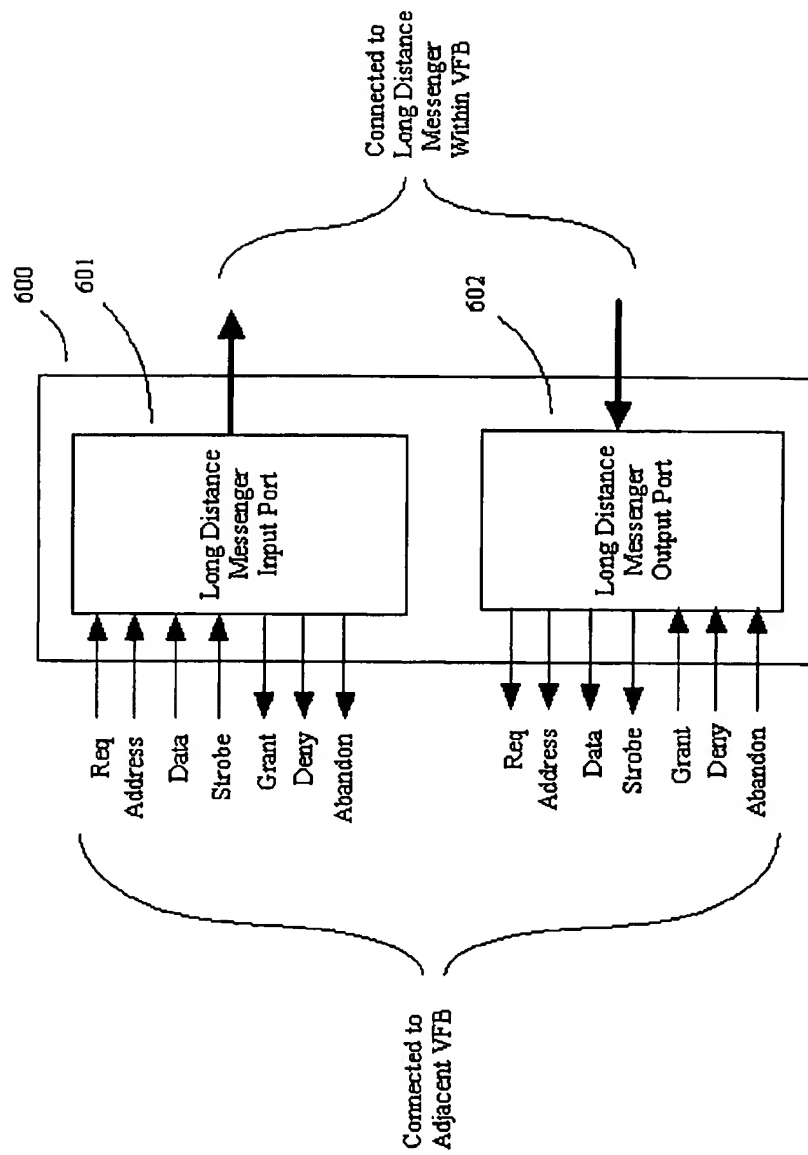


Figure 6, Long Distance Messenger Port



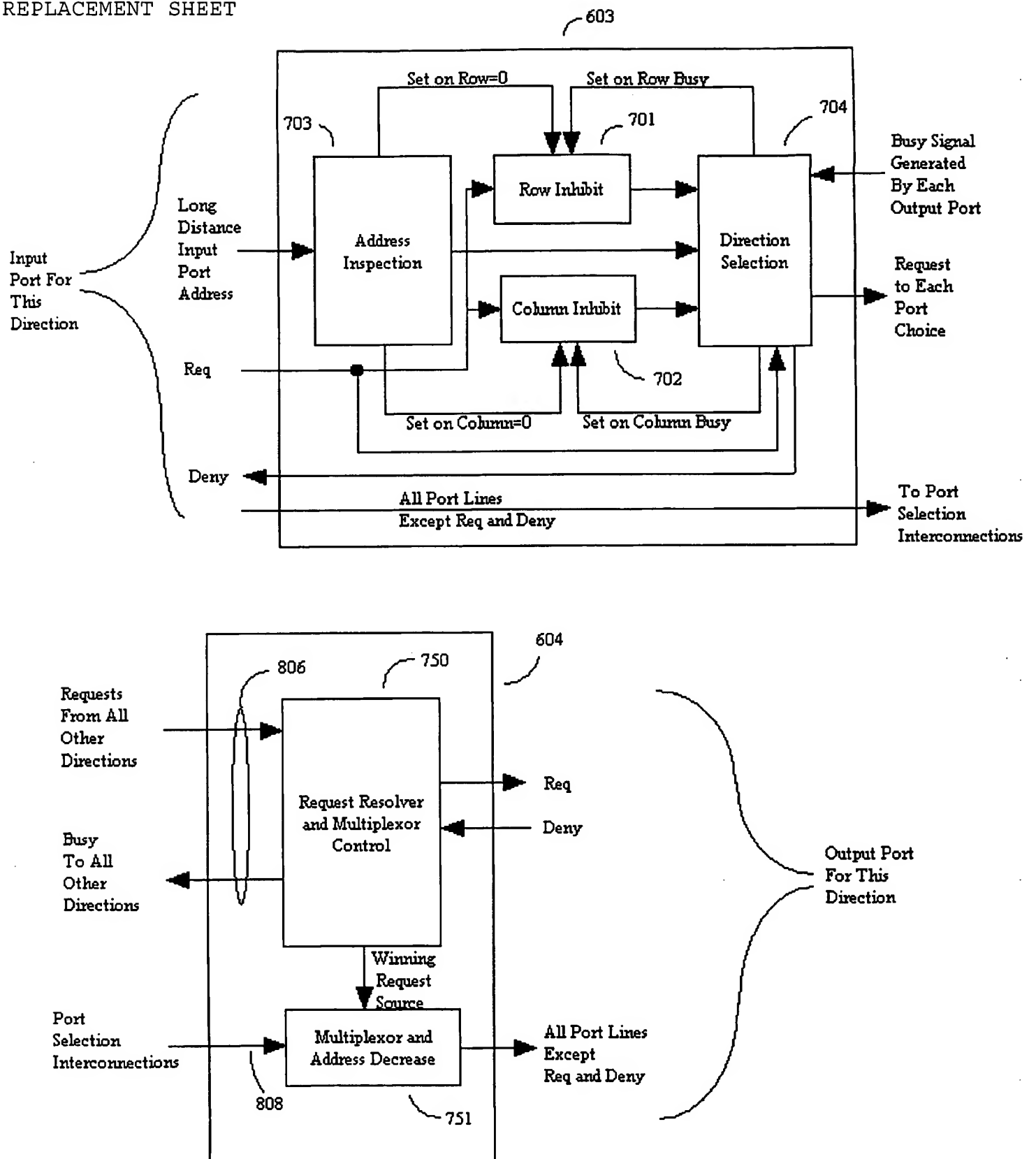


Figure 7, Long Distance Messenger Port Control Logic

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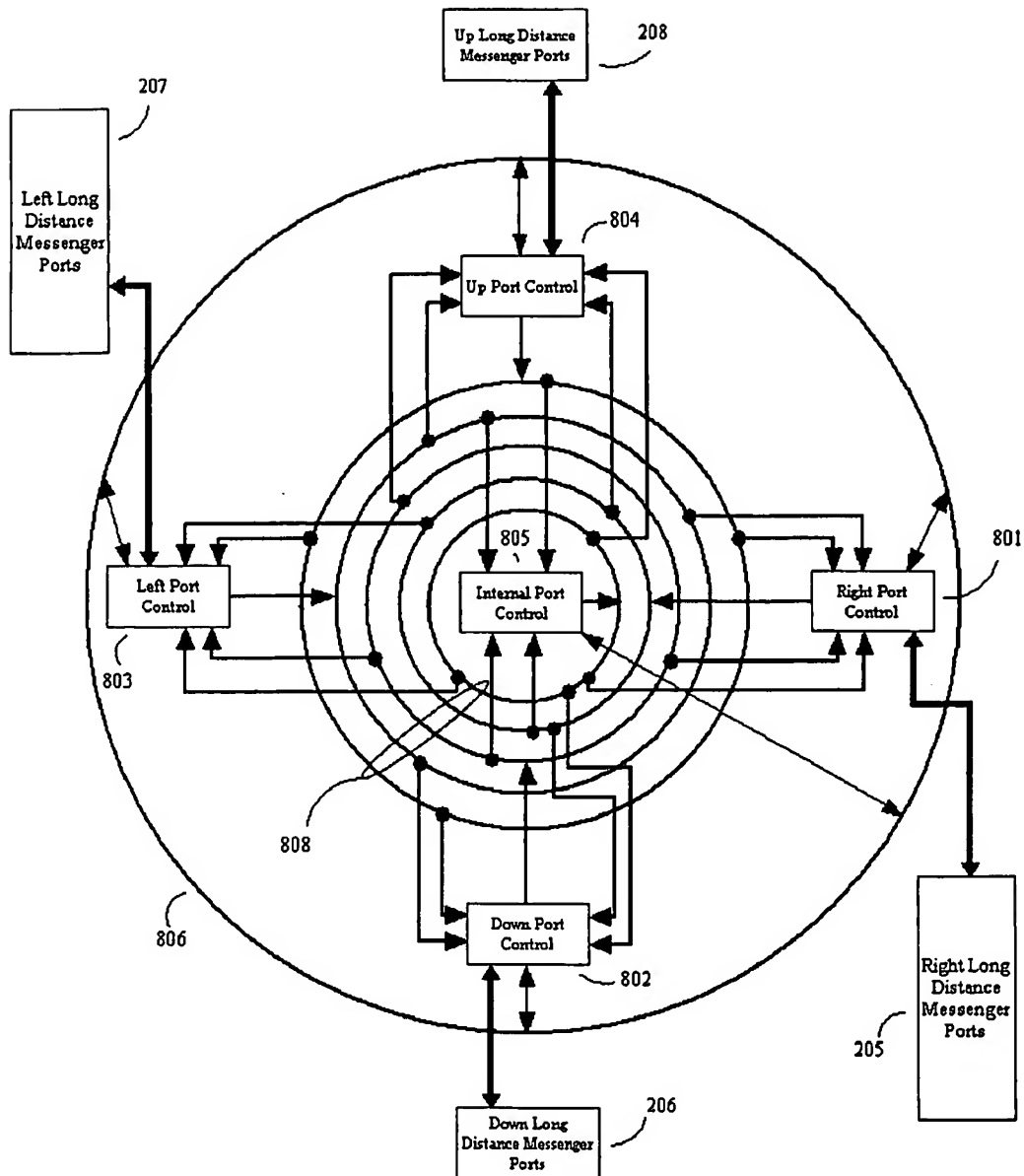


Figure 8, Long Distance Messenger

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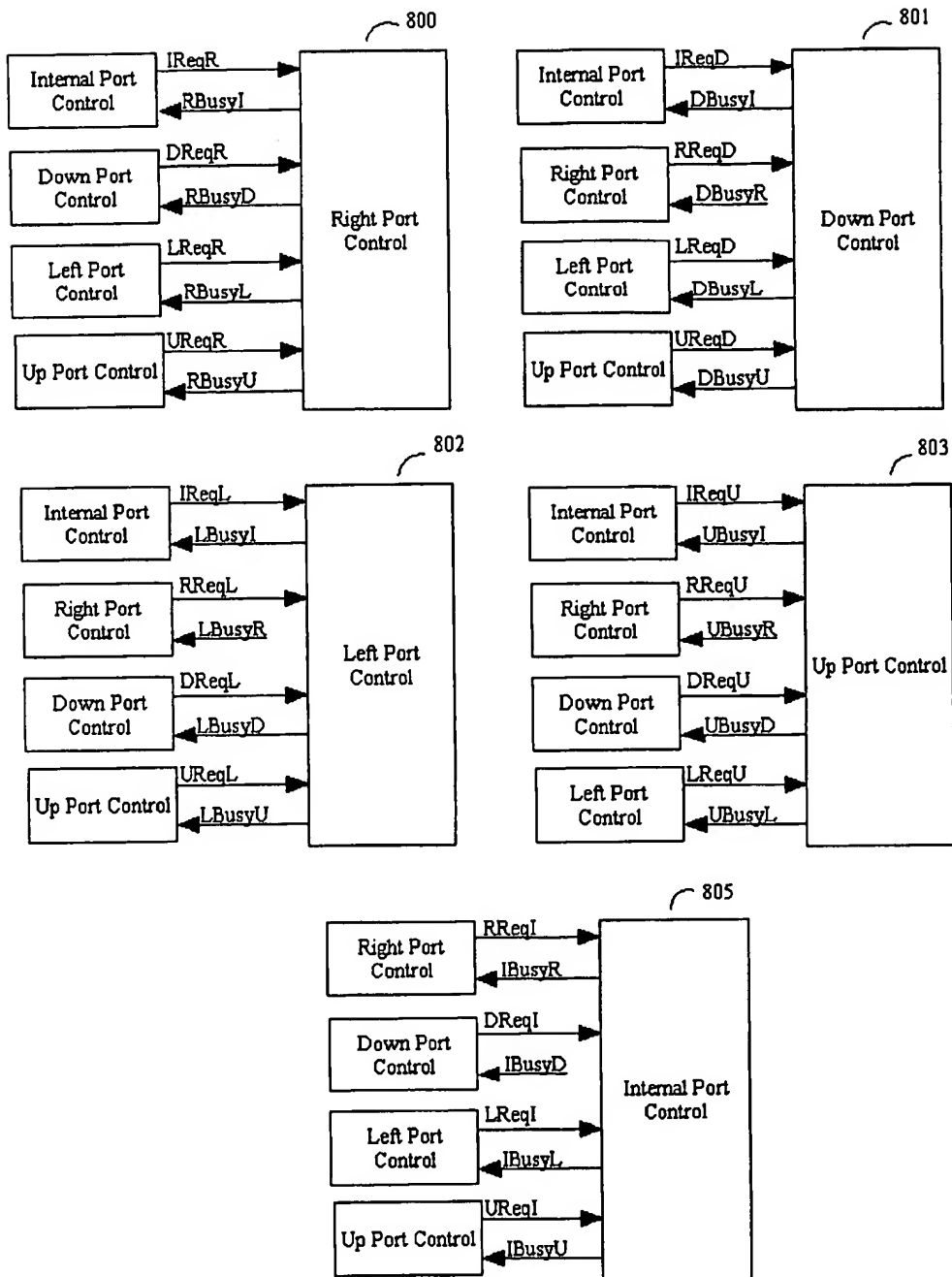


Figure 9, Request and Busy Information Shared Among Long Distance Port Controls

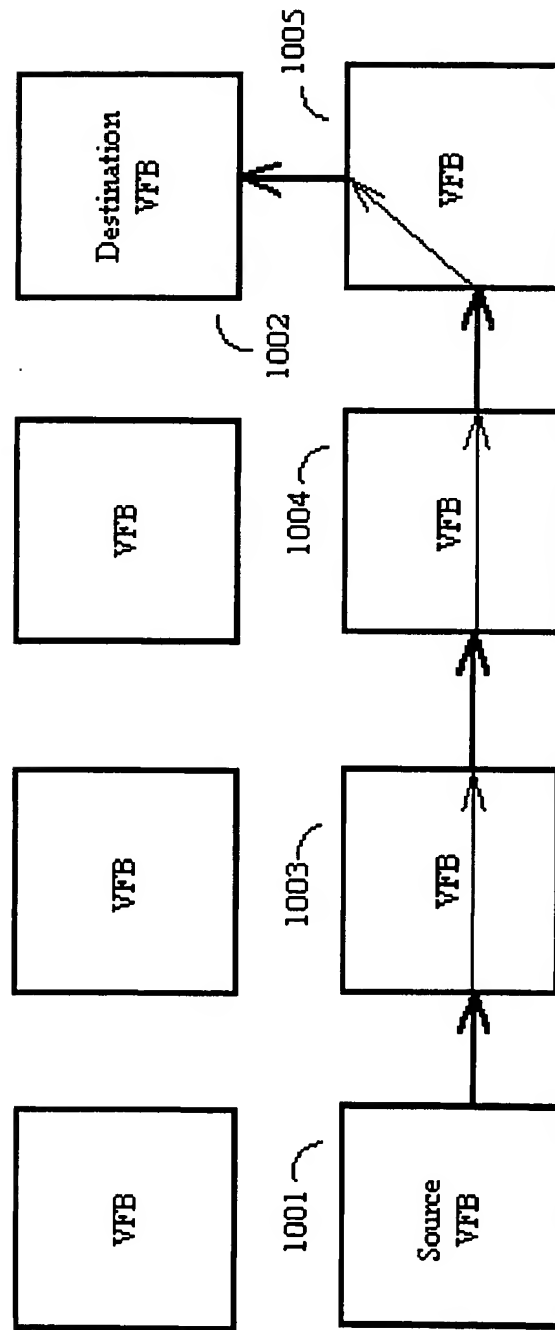


Figure 10, A Long Distance Message Connection, No Path Contention

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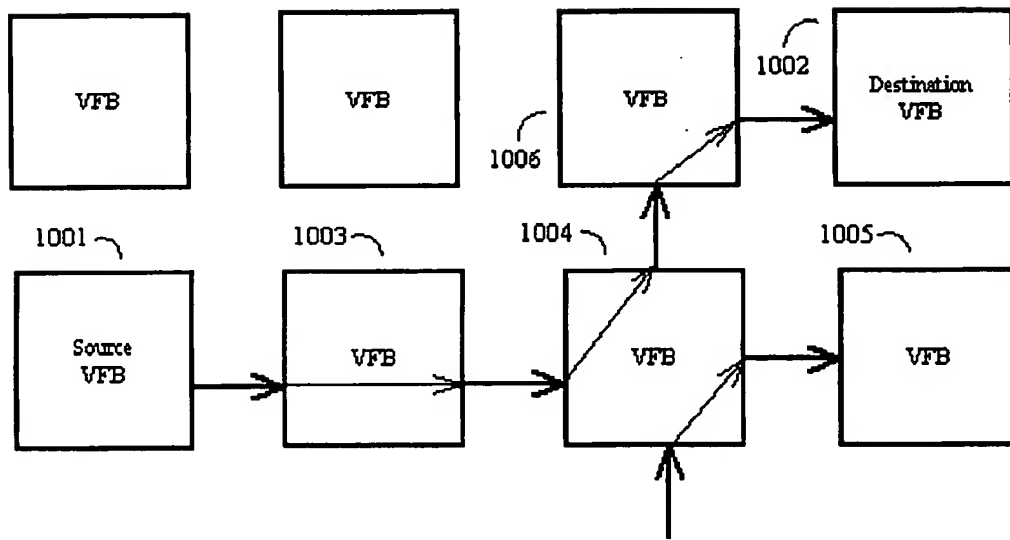


Figure 11, A Long Distance Message Connection, Path Contention Without Deny

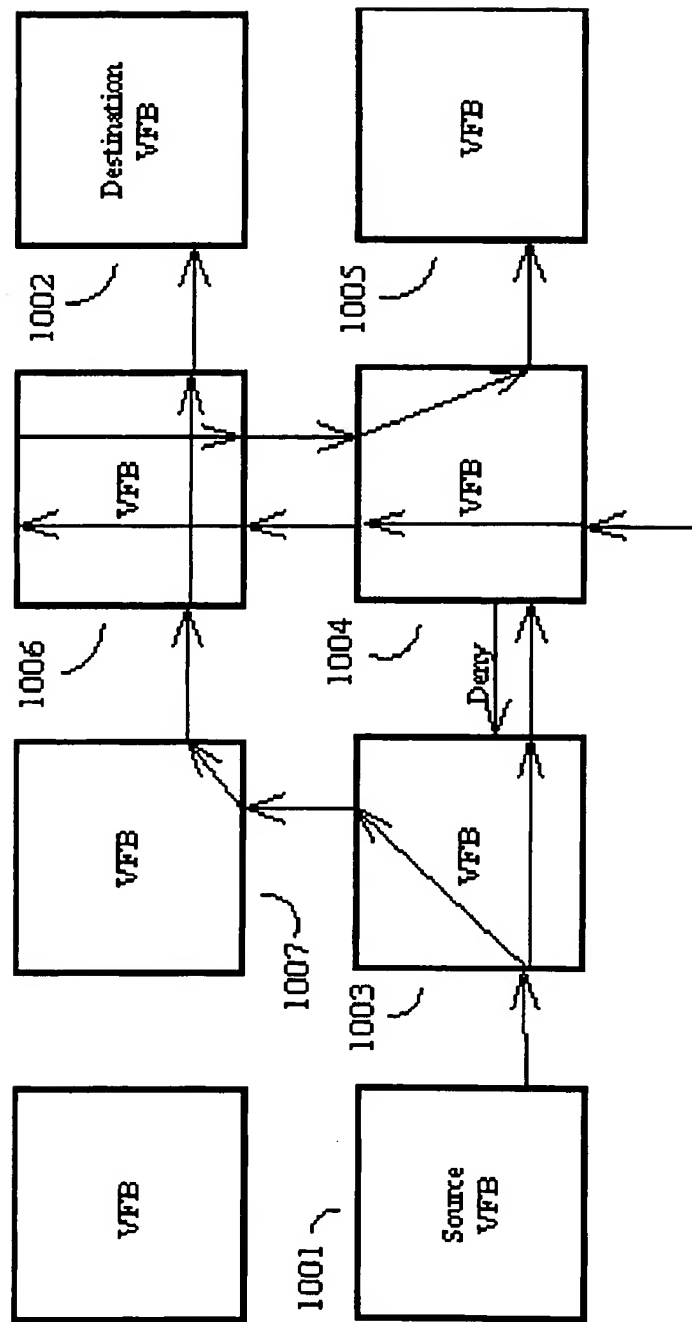
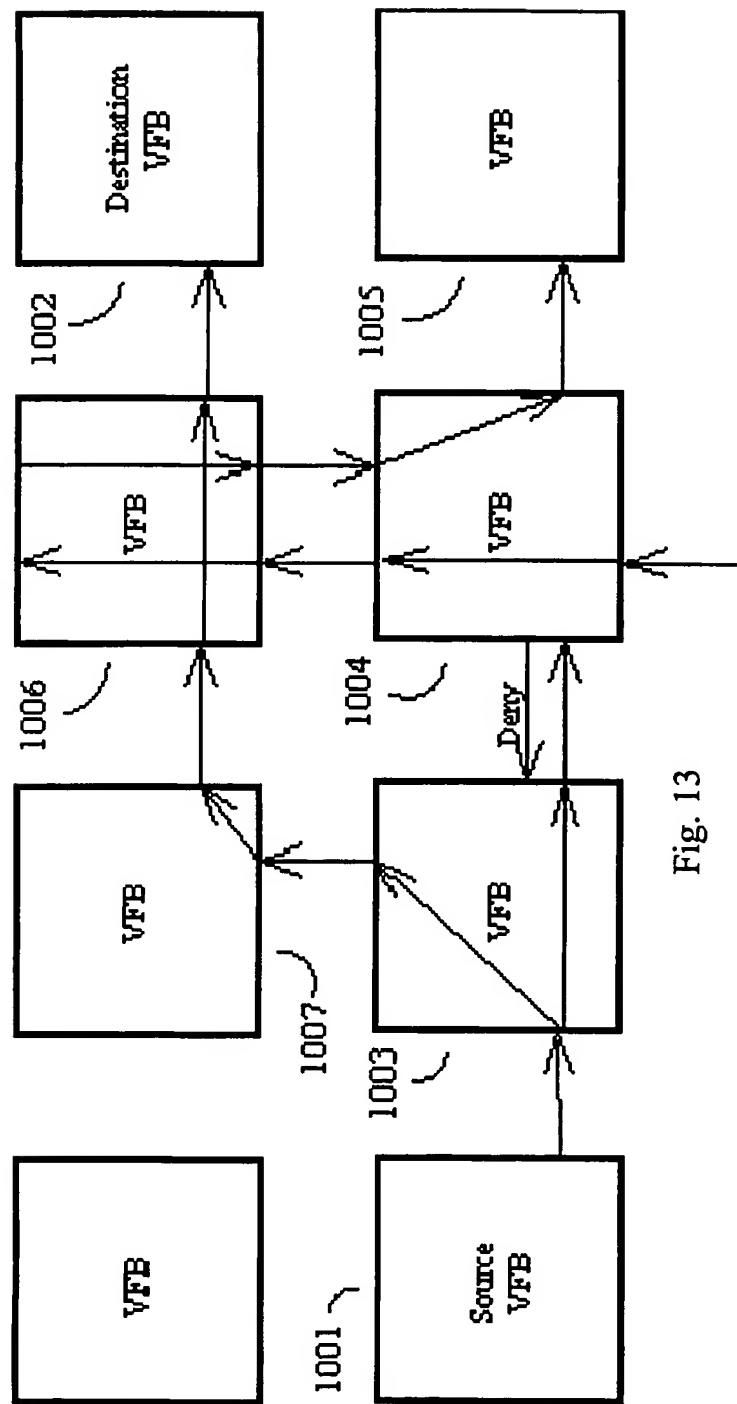


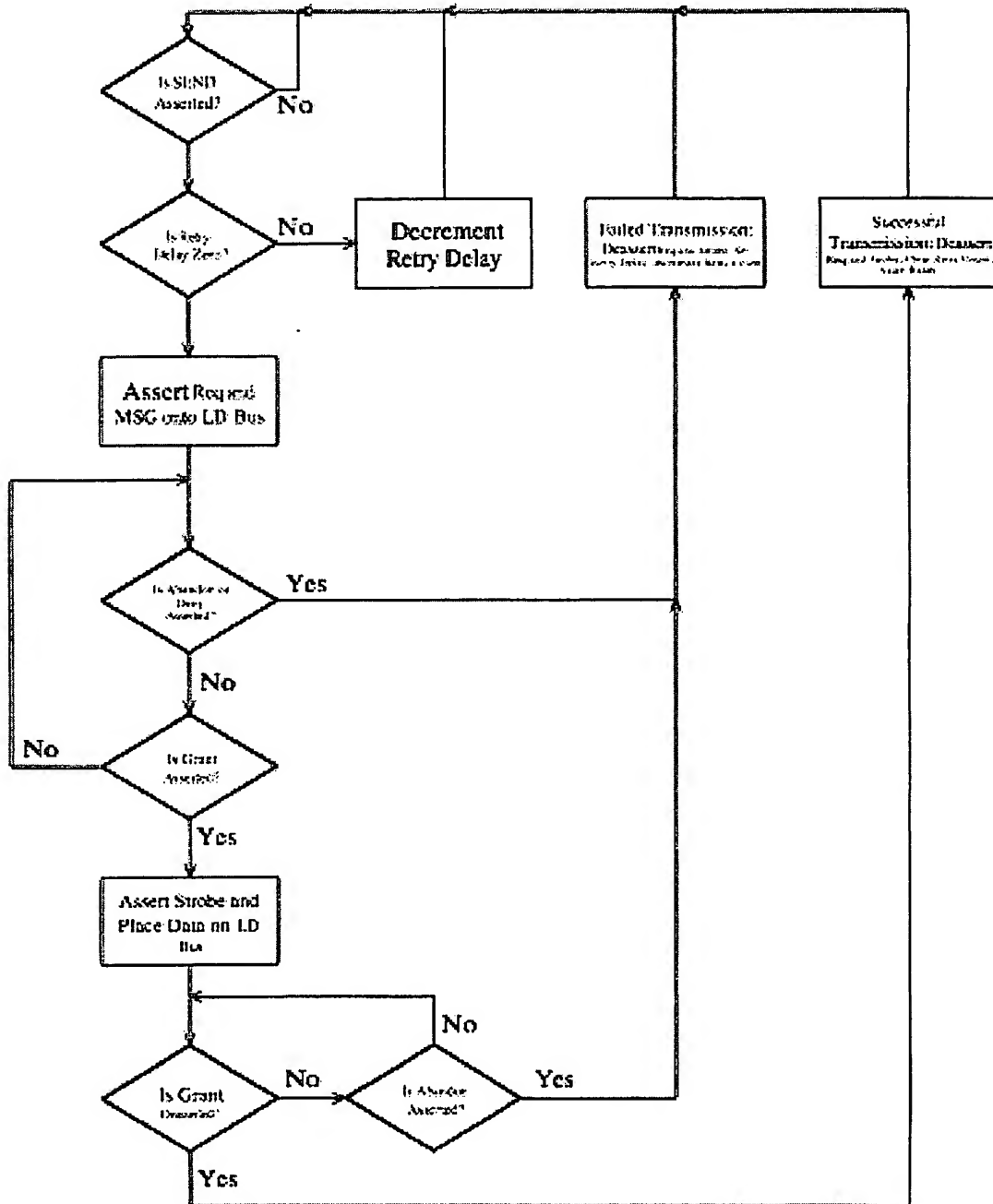
Figure 12, A Long Distance Message Connection, Path Contention With Deny

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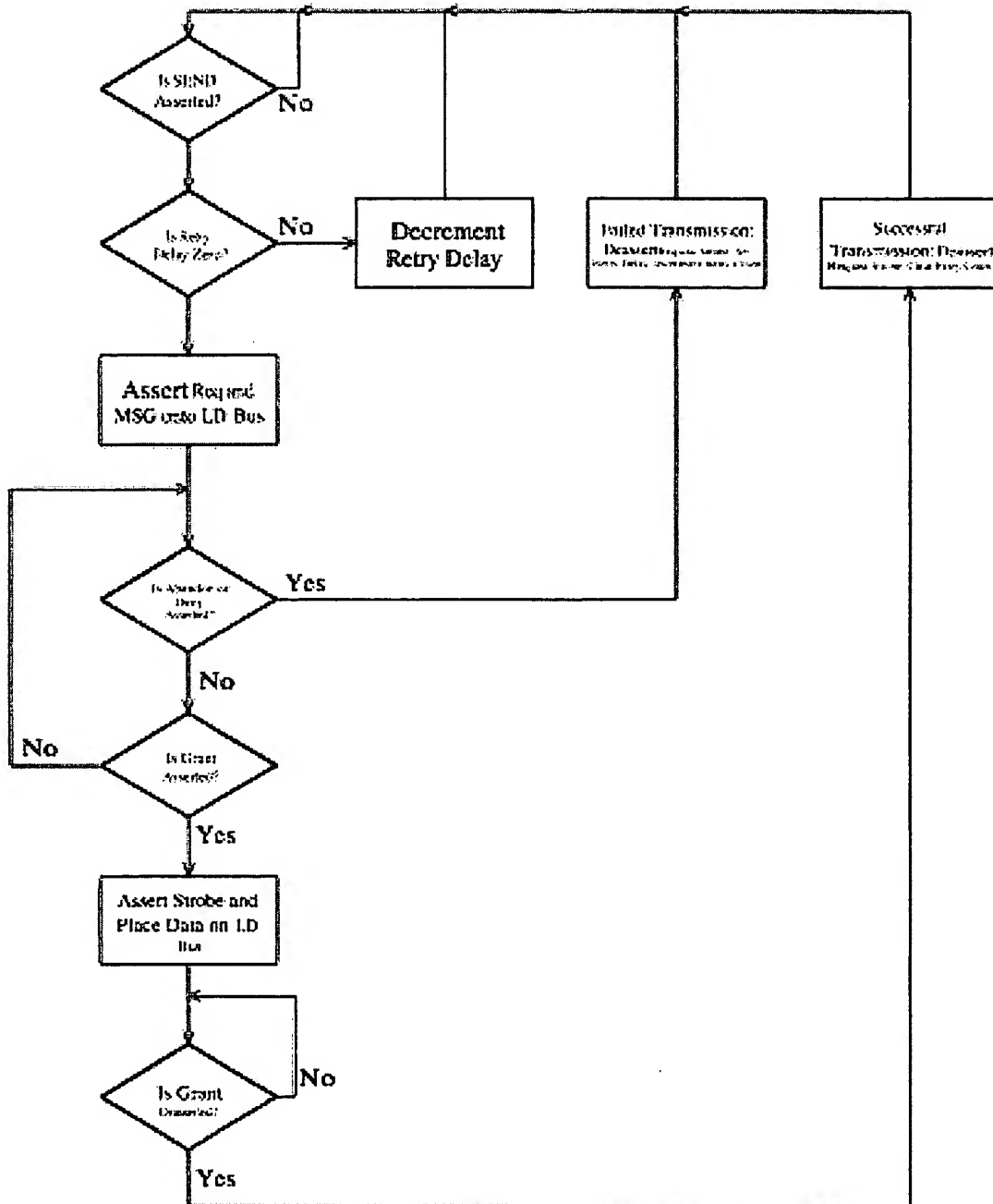
Fig. 14 Bridge Send Operation (Single Port, Optimistic Send)





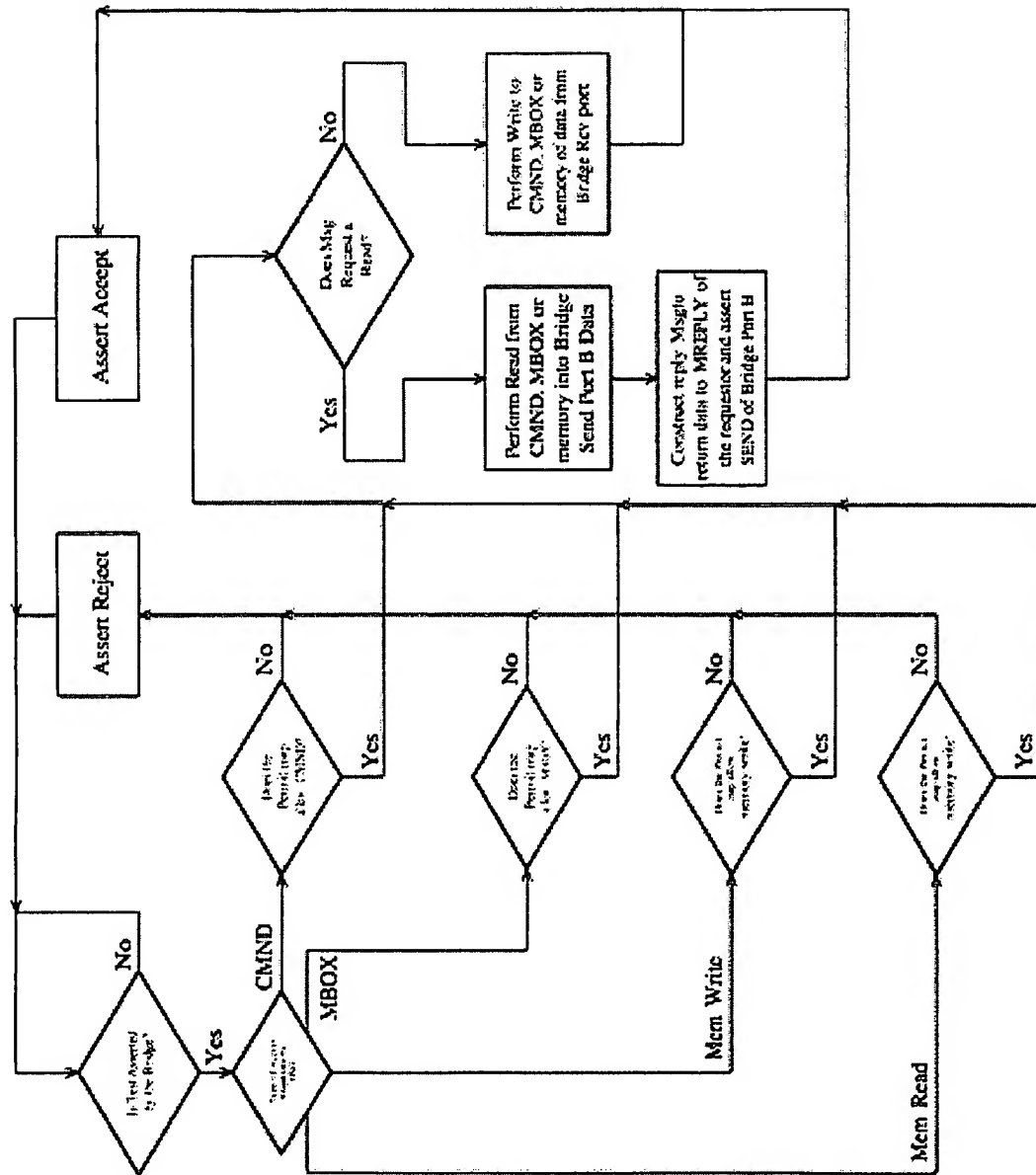
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Fig.15 Bridge Send Operation (Single Port, Pessimistic Send)



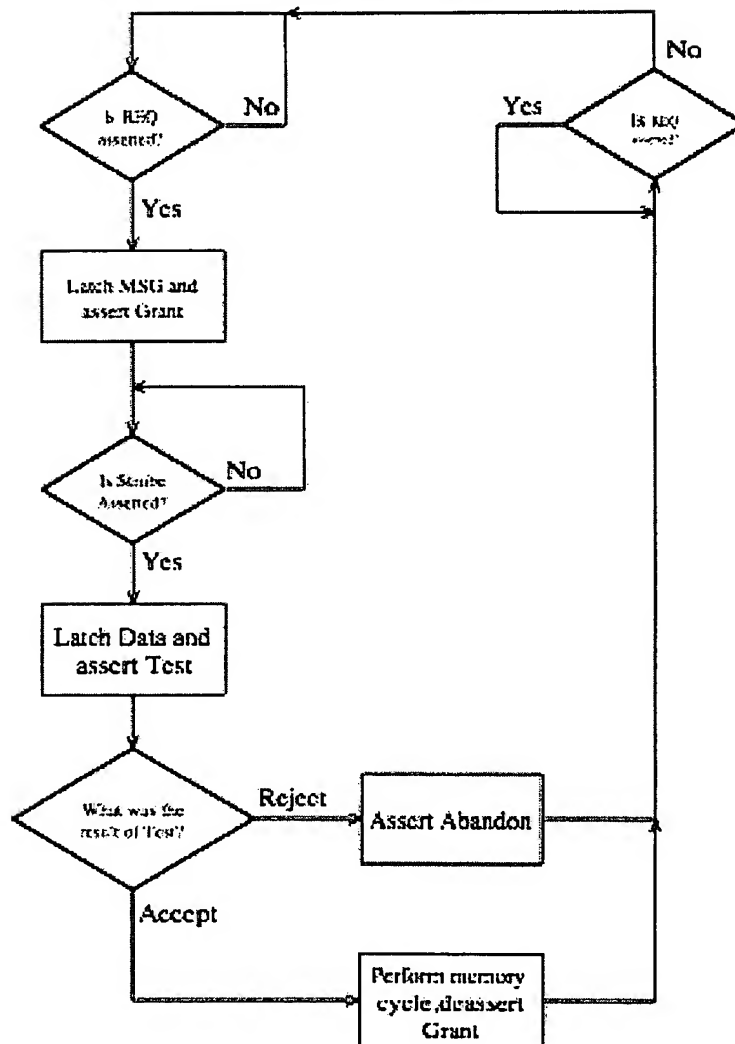
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**Fig. 16 Permissions Test (Dual Send Port Bridge)**



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Fig. 17 Bridge Receive (Optimistic)



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**Fig. 18 Bridge Receive (Pessimistic)**

